

Docket No.: M4065.0581/P581-A
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Zvi Regev, et al.

Application No.: Not Yet Assigned

Filed: October 5, 2003

Art Unit: N/A

For: A TERTIARY CAM CELL

Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT (IDS)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the new patent application submitted herewith.

Those patent(s) or publication(s) which are marked with an asterisk (*) next to the Cite No. in the attached form PTO/SB/08 are not supplied because they were previously cited by or submitted to the Office in a prior application number 10/202,003, filed July 25, 2002 and relied upon in this application for an earlier filing date under 35 U.S.C. 120.

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The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0581/P581-A. A duplicate copy of this paper is enclosed.

Dated: October 15, 2003

Respectfully submitted,

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Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	Not Yet Assigned
				Filing Date	October 15, 2003
				First Named Inventor	Zvi Regev
				Art Unit	N/A
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	M4065.0581/P581-A

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
<input checked="" type="checkbox"/>	AA	5,642,320	06/24/1997	Jang	
<input checked="" type="checkbox"/>	AB	6,331,961	12/18/2001	Kengeri et al.	
<input checked="" type="checkbox"/>	AC	4,622,653	11/11/1986	McElroy	
<input checked="" type="checkbox"/>	AD	5,642,322	06/24/1997	Yoneda	
<input checked="" type="checkbox"/>	AE	5,999,435	12/07/1999	Henderson et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
<input checked="" type="checkbox"/>	BA	JP 56029891	03/25/1981			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<input checked="" type="checkbox"/>	CA	MUHAMMAD M. KHELLAH, MOHAMED I. ELMASRY, "Low-Power Design of High-Capacitive CMOS Circuits Using a New Charge Sharing Scheme", 1999 IEEE International Solid State Circuits Conference.	
<input checked="" type="checkbox"/>	CB	BRUCE JACOB, "Cache Design for Embedded Real-Time Systems", Embedded Systems Conference, June 30, 1999.	
<input checked="" type="checkbox"/>	CC	"Implementing High-Speed Search Applications with APEX CAM", Altera Corporation, Application Note 119, July 1999, ver. 1.01.	

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.